

Mr. Xing Zheng

Mobile: 18612117960

E-Mail: eezhengx@gmail.com

Add: Dayuncun Apartment, Beihang University, 37 Xueyuan Road,
Haidian District, Beijing, China**EDUCATIONAL BACKGROUND**

- **Beihang University (985)** 09/2017-01/2020
Major: Control Science and Engineering
Degree: Master; **Mode of Education:** Full-time; **GPA:**3.57/4; **Major Ranking:** 2/20
Major courses: Mathematical Statistics, Modern Digital Signal Processing, Intelligent Control and Its Application, Matrix Theory
- **Ocean University of China (985)** 09/2013-06/2017
Major: Automation
Degree: Bachelor; **Mode of Education:** Full-time; **GPA:**3.49/4; **Major Ranking:** 1/80
Major courses: Database System, Fundamentals of Computers, Advanced Mathematics, Electric Circuit Principles, Signal and System, Control system Simulation and design, Digital signal processing, Theory of Automatic Control, Computer Control Technique, Electric Machine and Drive Control System
- **University of Malaya** 09/2018-01/2019
Major: Electronic Engineering
Mode of Education: Exchange Student (half a year)
- **Dalian University of Technology (985)** 09/2014-06/2015
Major: Electrical Engineering and Automation
Mode of Education: Exchange Student (a year)

RESEARCH

- **Design of LPR System based on FPGA** 07/2016 to 06/2017
Source: Undergraduate graduation project
Position: Person in Charge
Responsibility: 1. complete program design; 2. accomplish circuit design 3. complete FPGA programming 4. the final recognition accuracy rate was 90%, and won the title of excellent graduation design
- **Infrared Missile Seeker Calibration and Control System (12 million)** 12/2017 to 01/2019
Source: First Research Institute of Chinese Aerospace
Position: Group Leader
Responsibility: 1. design and implement the scheme 2. lead the team to construct the hardware platform 3. parse the digital signal protocol and convert to code 4. accomplish the entire software design, including interface design, signal process design, database design and driver design 5. achieve a series of automated process 6. make summary reports and gain the approval of the researchers
- **East Red Satellite IMU Calibration System (1.8 million)** 09/2018 to 03/2019
Source: Fifth Research Institute of Chinese Aerospace
Position: Group Leader

Responsibility: 1. finish the overall architecture design of this system 2. help the team members to complete the design of signal process card, achieve decoding and encoding this signal 3. design the system software framework of the measurement and control system

● **Missile Flight Control Module Measurement System (3.8million)** 03/2019 to 07/2019

Source: First Research Institute of Chinese Aerospace

Position: Group Leader

Responsibility: 1. design project scheme 2. help team members design ARINC429 communication card, 1553B communication card, 422 communication card, telemetry signal acquisition card, DA/AD card and digital IO card 3. construct the whole system, including the functions of data collection, decoding processing, encoding preservation, decoding and playback.

HONORS

- 2014: Third-class scholarship of Ocean university of China; Excellent student of Ocean University of China
- 2015 Second-class scholarship of Ocean university of China
- 2016: First-class scholarship of Ocean university of China; Excellent student of Ocean University of China;
- National Scholarship; The second prize of Shandong at "National Electronic Design Competition"
- 2017: Excellent graduate of Ocean University of China; Excellent graduation design of Ocean University of China; Excellent graduate of Shandong Province
- 2018: Second-class scholarship of Beihang University
- 2019: First-class scholarship of Beihang University; Excellent league member of Beihang University
- 2020: Excellent graduate of Beihang University

PRACTICAL EXPERIENCE & ACADEMIC ACHIEVEMENT

- using C/C++/C#/verilog/matlab/python to design computer software control systems
- designing specific circuits about digital signal processing
- leading team to carry on research and complete the whole scheme design independently

《Design and realize of integrated test system for flight control components based on PXI bus》